



NITTE
(Deemed to be University)

**NMAM INSTITUTE
OF TECHNOLOGY**

(Established under Section 3 of UGC Act 1956)

Nitte – 574 110, Karnataka, India

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Volume 04

Issue 04

November 2024

News Letter





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NPTEL Course Completion

24 November 2024

Dr. Girisha Joshi, Associate Professor, Department of Electrical and Electronics Engineering, NMAM Institute of Technology, Nitte, had successfully completed the NPTEL course “C Programming and Assembly Language” held during Aug-Sep 2024.



NPTEL-AICTE Faculty Development Programme

(Funded by the MoE, Govt. of India)



This certificate is awarded to

GIRISHA JOSHI

for successfully completing the course

C Programming and Assembly Language

with a consolidated score of **67 %**



Prof. Andrew Thangaraj
NPTEL Coordinator
IIT Madras



(Aug-Sep 2024)

Roll No: NPTEL24CS128S565500027

Duration of NPTEL course : 4 Weeks

The candidate has studied the above course through MOOCs mode, has submitted online assignments and passed proctored exams.
This certificate is therefore acceptable for promotions under CAS as per AICTE notifications dated 16th Nov, 2023, similar to other refresher / orientation courses.
F.No. AICTE / RIFD / FDP through MOOCs / 2023

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**Details of the Workshop / STTP/ Faculty Development Program
attended by the faculty of E&E Engg in the month of October /
November 2024**

Sl. No.	Name	Dept	Title of the Programme	Place	Date
1.	Dr. Girisha Joshi	EEE	SWAYAM Course on “C Programming and Assembly Language”	Online	Aug – Sep 2024
2.	Mrs. Nutana Shetty	EEE	Advance Trends in Microgrids, Smart Grids & Electric Vehicles	Online	21-25 October 2024
3.	Mrs. Swathi K	EEE	Advance Trends in Microgrids, Smart Grids & Electric Vehicles	Online	21-25 October 2024
4.	Dr. Nandini K K	EEE	Advance trends in Microgrids, Smart grids and Electric vehicles	Stanley College of engineering and Technology, Hyderabad	21-25 October 2024
5.	Mr. Ravikiran Rao M	EEE	FPGA Based Digital Design using Real Digital Boolean Boards	SDC, NITTE(DU)	28-30 October 2024
6.	Dr. Anitha Marina Colaco	EEE	FPGA Based Digital Design using Real Digital Boolean Boards	SDC, NITTE(DU)	28-30 October 2024
7.	Dr. Rajalakshmi Samaga BL	EEE	FPGA Based Digital Design using Real Digital Boolean Boards	SDC, NITTE(DU)	28-30 October 2024
8.	Dr. K. Latha Shenoy	EEE	FPGA Based Digital Design using Real Digital Boolean	SDC, NITTE(DU)	28-30 October 2024

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Sl. No.	Name	Dept	Title of the Programme	Place	Date
			Boards		
9.	Mrs. Raksha Adappa	EEE	FPGA Based Digital Design using Real Digital Boolean Boards	SDC, NITTE(DU)	28-30 October 2024
10.	Mrs. Swathi Hatwar H	EEE	FPGA Based Digital Design using Real Digital Boolean Boards	SDC, NITTE(DU)	28-30 October 2024
11.	Mr. Mahabaleshwar a Sharma K	EEE	Classroom Management Series	SDC, NITTE(DU)	29 October 2024
12.	Dr. Dinesh Shetty	EEE	FPGA Based Digital Design using Real Digital Boolean Boards	SDC, NITTE(DU)	28-30 October 2024
13.	Dr. Dinesh Shetty	EEE	Inferential Statistics	Coursera	7 November 2024
14.	Mrs. Nutana Shetty	EEE	Classroom Management Series	SDC, NITTE(DU)	15 November 2024
15.	Dr. Dinesh Shetty	EEE	Electronics System Design: Handson circuit & PCB Design	NPTEL FDP	16 November 2024